

AMENDMENTS TO THE CLAIMS:

Please amend the claims as follows:

Claim 1. (Currently Amended) A termination resistor circuit, provided in an interface circuit through which signals are transferred, comprising:

a first termination resistor block having a first transistor which is a diode-connected transistor, and a second transistor which is not a diode-connected transistor; and

a second termination resistor block having the second transistor and a third transistor which is not a diode-connected transistor, the second transistor being used in common by said first termination resistor block and said second termination resistor block;

wherein said first termination resistor block and said second termination resistor block are connected at terminals of the second transistor; and wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.

Claim 2. (Previously Presented) The termination resistor circuit as claimed in claim 1, wherein said first termination resistor block comprises transistors of a same conductivity type; and wherein said second termination resistor block comprises transistors of different conductivity types.

Claim 3. (Currently Amended) A termination resistor circuit provided in an interface circuit, through which signals are transferred via a transmission line, comprising:

a first termination resistor block having a first transistor and a second transistor, a gate of the first transistor being applied with a reference voltage or a voltage of said transmission line, and a gate of the second transistor being not applied with the reference voltage nor the voltage of said transmission line; and

a second termination resistor block having the second transistor and a third transistor whose gate is not applied with the reference voltage nor the voltage of said transmission line, the second transistor being used in common by said first termination resistor block and said second termination resistor block;

wherein said first termination resistor block and said second termination resistor block are connected at terminals of said the second transistor; and wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.

Claim 4. (Previously Presented) The termination resistor circuit as claimed in claim 3, wherein said first termination resistor block comprises transistors of a same conductivity type; and wherein said second termination resistor block comprises transistors of different conductivity types.

Claim 5. (Currently Amended) A termination resistor circuit provided in an interface circuit through which signals are transferred, comprising:

a first termination resistor block; and

a second termination resistor block coupled in parallel with said first terminal resistor block;

wherein said first termination resistor block differs from said second termination resistor block by including a first transistor whose gate is connected to its drain;

wherein said first termination resistor block further includes a second transistor whose gate is not connected to its drain;

wherein said second termination resistor block includes the second transistor and a third transistor whose gate is not connected to its drain;

wherein the second transistor is used in common by said first termination resistor block and said second termination resistor block; and

wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.

Claim 6. (Previously Presented) The termination resistor circuit as claimed in claim 4, wherein said first and second transistors are chosen to have a size for

said first termination resistor block so that said first termination resistor block has a respectively chosen weight.

Claim 7. (Previously Presented) The termination resistor circuit as claimed in claim 4, wherein said first, second and third transistors are chosen to be substantially equal in size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have the same weight.

Claim 8. (Previously Presented) The termination resistor circuit as claimed in claim 4, wherein said first, second and third transistors are chosen to have a size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have respectively chosen weights.

Claim 9. (Canceled)

Claim 10. (Currently Amended) A signal transmission system, comprising:

a transmitting circuit for transmitting a signal;

a transmission line for transmitting the signal output from said transmitting circuit;

a receiving circuit for, receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a first transistor which is a diode-connected transistor and a second transistor which is not a diode-connected transistor; and

a second termination resistor block having the second transistor and a third transistor which is not a diode-connected transistor, the second transistor being used in common by said first termination resistor block and said second termination resistor block;

wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.

Claim 11. (Previously Presented) The signal transmission system as claimed in claim 10, wherein said first termination resistor block comprises transistors of a same conductivity type; and wherein said second termination resistor block comprises transistors of different conductivity types.

Claim 12. (Currently Amended) A signal transmission system, comprising:

a transmitting circuit for transmitting out a signal;

a transmission line for transmitting therethrough the signal output from said transmitting circuit;

a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a first transistor and a second transistor, a gate of the first transistor being applied with a reference voltage or a voltage of said transmission line, and a gate of the second transistor being not applied with the reference voltage nor the voltage of said transmission line; and

a second termination resistor block having the second transistor and a third transistor whose gate is not applied with the reference voltage nor the voltage of said transmission line, the second transistor being used in common by said first termination resistor block and said second termination resistor block;

wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.

Claim 13. (Previously Presented) The signal transmission system as claimed in claim 12, wherein said first termination resistor block comprises

transistors of a same conductivity type; and wherein said second termination resistor block comprises transistors of different conductivity types.

Claim 14. (Currently Amended) A signal transmission system comprising:

a transmitting circuit for transmitting a signal;

a transmission line for transmitting the signal output from said transmitting circuit;

a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block; and

a second termination resistor block;

wherein said first termination resistor block differs from said second termination resistor block by including a first transistor whose gate is connected to its drain;

wherein said first termination resistor block further includes a second transistor whose gate is not connected to its drain;

wherein said second termination resistor block includes the second transistor and a third transistor whose gate is not connected to its drain, the second

transistor being used in common by said first termination resistor block and said second termination resistor block; and

wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.

Claim 15. (Previously Presented) The signal transmission system as claimed in claim 13, wherein said first and second transistors are chosen to have a size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have respectively chosen weights.

Claim 16. (Previously Presented) The signal transmission system as claimed in claim 13, wherein said second and third transistors are chosen to be substantially equal in size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have the same weight.

Claim 17. (Previously Presented) The signal transmission system as claimed in claim 13, wherein said second and third transistors are chosen to have a size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have respectively chosen weights.



Claim 18. (Canceled)

Claim 19. (Currently Amended) A signal transmission system, comprising:

a transmission line for transmitting a signal;

a receiving circuit for receiving the signal transmitted through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a first transistor which is a diode-connected transistor, and a second transistor which is not a diode-connected transistor; and

a second termination resistor block having the second transistor and a third transistor which is not a diode-connected transistor, the second transistor being used in common by said first termination resistor block and said second termination resistor block;

wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.

Claim 20. (Previously Presented) The signal transmission system as

claimed in claim 19, wherein said first termination resistor block comprises transistors of a same conductivity type; and wherein said second termination resistor block comprises transistors of different conductivity types.

Claim 21. (Currently Amended) A signal transmission system, comprising:

a transmission line for transmitting a signal;

a receiving circuit for receiving the signal transmitted through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a first transistor and a second transistor, a gate of the first transistor being applied with a reference voltage or a voltage of said transmission line, and a gate of the second transistor being not applied with the reference voltage nor the voltage of said transmission line; and

a second termination resistor block having the second transistor and a third transistor whose gate is not applied with the reference voltage nor the voltage of said transmission line, the second transistor being used in common by said first termination resistor block and said second termination resistor block;

wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.

Claim 22. (Previously Presented) The signal transmission system as claimed in claim 21, wherein said first termination resistor block comprises transistors of a same conductivity type; and wherein said second termination resistor block comprises transistors of different conductivity types.

Claim 23. (Currently Amended) A signal transmission system, comprising:

- a transmitting circuit for transmitting a signal;

- a transmission line for transmitting the signal output from said transmitting circuit;

- a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

- a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

- a first termination resistor block having a first transistor which is a diode-connected transistor, and a second transistor which is not a diode-connected transistor; and

a second termination resistor block having the second transistor and a third transistor which is not a diode-connected transistor, the second transistor being used in common by said first termination resistor block and said second termination resistor block;

wherein said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block by switching between said first transistor and said third transistor.